Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)
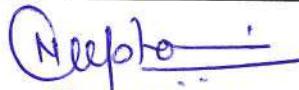
Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (Electronics & Communication)

VII - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
							Theory			Practical			
				L	T	P	End Sem Unversity Exam	Two Term Exam	Teachers Asses-ment*	End Sem Unversity Exam	Teachers Asses-ment*		
1	BTEC701	Optical & Satellite Communication		3	1	0	60	20	20	0	0	100	4
2	BTEC702	Advanced Communication System		3	0	2	60	20	20	30	20	150	4
3	BTEC713/ BTEC723/ BTEI601	Electives: 1 Wireless Network 2 Advanced Microcontroller & Embedded Systems 3 Biomedical Instrumentation		3	1	2	60	20	20	30	20	150	5
4	BTEC714/ BTEI604/ BTCSIOT 601	Electives: 1 Embedded Systems in Robotics 2 Digital Image Processing 3 Web Programming		3	1	2	60	20	20	30	20	150	5
5	BBAI501	Human Values & Professional Ethics		4	0	0	60	20	20	0	0	100	4
6	BTEC705	Major Project		0	0	8	0	0	0	90	60	150	4
		TOTAL		15	4	14	300	100	100	180	120	800	26

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Registrar
Shri Vaishnav Vidyapeeth Vishwavidyalaya
INDORE (M.P.)